

ELECTRICAL CHARACTERISTICS

The ● denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25^\circ\text{C}$. $V_{IN} = 12\text{V}$, $\text{EN/UVLO} = 12\text{V}$ unless otherwise noted.

PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
V_{IN} Operating Voltage Range		●	2.8		60	V
V_{IN} Quiescent Current at Shutdown	$\text{V}_{\text{EN/UVLO}} = 0.2\text{V}$	●		1	2	μA
	$\text{V}_{\text{EN/UVLO}} = 1.5\text{V}$	●		1	15	μA
V_{IN} Quiescent Current	$\text{V}_{\text{EN/UVLO}} = 1.5\text{V}$	●	2	5	25	μA
		●	2		25	μA
Sleep Mode (Not Switching)	$\text{SYNC} = 0\text{V}$	●	9	15		μA
		●	9	30		μA
Active Mode (Not Switching)	$\text{SYNC} = 0\text{V}$ or INTV_{CC} , $\text{BIAS} = 0\text{V}$	●	1200	1600		μA
		●	1200	1850		μA
BIAS Threshold	$\text{SYNC} = 0\text{V}$ or INTV_{CC} , $\text{BIAS} = 5\text{V}$	●	22	40		μA
		●	22	65		μA
BIAS Threshold	Rising, BIAS Can Supply INTV_{CC}		4.4	4.65		V
	Falling, BIAS Cannot Supply INTV_{CC}		4	4.25		V
V_{IN} Falling Threshold to Supply INTV_{CC}	$\text{BIAS} = 12\text{V}$			$\text{BIAS} - 2\text{V}$		V
BIAS Falling Threshold to Supply INTV_{CC}	$V_{IN} = 12\text{V}$			V_{IN}		V
FBX Regulation						
FBX Regulation Voltage	$\text{FBX} > 0\text{V}$	●	1.568	1.6	1.632	V
	$\text{FBX} < 0\text{V}$	●	-0.820	-0.80	-0.780	V
FBX Line Regulation	$\text{FBX} > 0\text{V}$, $2.8\text{V} < V_{IN} < 60\text{V}$		-0.822	0.005	0.015	%/V
	$\text{FBX} < 0\text{V}$, $2.8\text{V} < V_{IN} < 60\text{V}$			0.005	0.015	%/V
FBX Pin Current	$\text{FBX} = 1.6\text{V}, -0.8\text{V}$	●	-10		10	nA
Oscillator						
Switching Frequency (fosc)	$R_T = 165\text{k}$	●	273	300	327	kHz
	$R_T = 45.3\text{k}$	●	0.92	0.90	1	MHz
	$R_T = 20\text{k}$	●	1.85	2	2.15	MHz
SSFM Maximum Frequency Deviation	$(\Delta f/f_{\text{osc}}) \cdot 100$, $R_T = 20\text{k}$		14	20	25 28	%
Minimum On-Time	Burst Mode, $V_{IN} = 24\text{V}$ (Note 6)			70	95	ns
	Pulse-Skip Mode, $V_{IN} = 24\text{V}$ (Note 6)			70	90	ns
Minimum Off-Time		●		55	75	ns
SYNC/Mode, Mode Thresholds (Note 5)	High (Rising), $V_{IN} = 24\text{V}$	●		1.3	1.7	V
	Low (Falling), $V_{IN} = 24\text{V}$	●	0.14	0.2		V
SYNC/Mode, Clock Thresholds (Note 5)	Rising, $V_{IN} = 24\text{V}$	●		1.3	1.7	V
	Falling, $V_{IN} = 24\text{V}$	●	0.4	0.8		V
$f_{\text{SYNC}}/f_{\text{osc}}$ Allowed Ratio	$R_T = 20\text{k}$		0.95	1	1.25	kHz/kHz
SYNC Pin Current	$\text{SYNC} = 2\text{V}$			10	25	μA
	$\text{SYNC} = 0\text{V}$, Current Out of Pin			10	25	μA
Switch						
Maximum Switch Current Limit Threshold		●	2	2.5	3.4	A
Switch Overcurrent Threshold	Discharges SS Pin			3.75		A
Switch $R_{DS(\text{ON})}$	$I_{SW} = 0.5\text{A}$			375		$\text{m}\Omega$
Switch Leakage Current	$V_{SW} = 100\text{V}$			0.1	1	μA